What is claimed is:

1. A capacitor type of microphone comprising:

a movable electrode vibrating in response to a sound vibration;

a fixed electrode arranged face to face to the movable electrode;

first amplification means for buffer-amplifying a terminal voltage between the movable electrode and the fixed electrode; and

second amplification means cascaded to the first amplification means between an output terminal of the first amplification means and a microphone output terminal.

2. The microphone according to claim 1, wherein the second amplification includes drive means, in which a power supply to the drive means is configured so that the power is obtained as a constant current from outside the microphone via the microphone output terminal.

3. The microphone according to claim 1, wherein the second amplification includes drive means, in which a power supply to the drive means is configured so that the power is temporarily obtained for storage through the microphone output terminal according to voltage values and the stored voltage is used when obtaining the power is stopped.

4. The microphone according to claim 1, wherein the second amplification means is composed of an FET (field effect transistor) structured into a gate-common amplification circuit, a source electrode of the FET receiving an output current of the first amplification means and a drain current of the FET passing to the microphone output terminal.

5. The microphone according to claim 1, wherein the second amplification means is composed of a junction type of transistor structured into a base-common amplification

circuit, an emitter electrode of the transistor receiving an output current of the first amplification means and a collector current of the transistor passing to the microphone output terminal.

6. The microphone according to claim 4, wherein the second amplification means is composed of the FET of which gate is connected to a common output terminal of the first amplification means, the source electrode of the FET receiving the output current of the first amplification means and the drain current of the FET passing to the microphone output terminal.

7. The microphone according to claim 1, wherein the first amplification means is composed of an FET (field effect transistor).